

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

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Application No.: New U.S. Patent Application

Filed: June 7, 2005

Docket No.: 124144

For: SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. The references 4-8 were cited in a counterpart foreign application. An English language version of the foreign search report is attached for the Examiner's information.
- ☒ 3. The references 1-3, 5, 7 and 9-12 were cited in the International Search Report. An English language version of the International Search Report is attached for the Examiner's information.
- ☒ 4. In accordance with 37 CFR §1.98(a)(2)(ii), copies of any U.S. patents and patent application publications are not attached.
- ☒ 5. English language Abstracts of the non-English language references 5-6 and 9-10 are attached hereto.

10/537809

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- ☒ 6. A computer-generated English language translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office ([http://www.jpo.go.jp]), and is attached, but has not been reviewed for accuracy. See Reference 2.
- ☒ 7. A partial translation of reference 5 is attached.

Respectfully submitted,

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JAO:EDM/emt

Date: June 7, 2005

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